smenor

Scott Menor

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EXPERTISE

- Algorithms
- App Development
- Bioinformatics
- Cell Culture
- Compilers
- Control Theory
- Cloud Infrastructure
- Data Structures
- Domain Specific Languages
- High Performance Computing
- Machine Learning
- Manufacturing
- Mechatronics
- Programming
- Prototyping
- Private Pilot
- Research
- Sensor Fusion
- Statistical Methods
- System Architecture
- Technical Strategy
- Unix/Operating Systems

LANGUAGES

- 中国人
- Expañol
- Français
- 日本語

EXPERIENCE

Assistant Research Scientist — **Arizona State University** — **2023-present**

Work on co-evolution and「drift-barrier hypothesis」

Co-Founder and CTO — **SILSYNC** — **2019-2023**

Conceived and developed a Project Breakdown Structure (PBS) based asynchronous hardware design collaboration declarative system description language and SwiftHW declarative system design language and design compiler

Co-Founder and Lead Cyberneticist — **Roambotics** — **2013-2023**

Developed systems and architecture for use in practical general purpose personal robots including machine learning, control theory, power-systems, mechatronics, software, API/SDK development, and OS Architecture

Research Scientist — **Arizona State University** — **2009-2013**

Developed and consulted on massively-parallel distributed data-structures and algorithms; HPC Fabric; co-created/taught graduate level course in high-performance computing

Research Scientist — **iPlant Collaborative** — **2007-2009**

Facilitated communication between software engineers and biologists to help with planning and building long-term project and collaboration strategy.

Research Assistant — **Arizona State University** — **2004-2007**

Worked on first-principles molecular thermodynamics and developed a scalable, distributed statistical method for inferring hierarchical mobility in macro-molecular ensembles and other large collections of tracked moving objects.

Contract Engineer — **Intel** — **2000-2001; 1994-1995**

Modelled and implemented neuromorphic function blocks for embedded microcontrollers. Successfully ported a mission-critical mixed-mode IA-32/IA-64 assembler from HP-UX to Linux for the Itanium project.

Developed and tested neural network function blocks for embedded microcontrollers

Junior Researcher — **University of Hawai`i at Mānoa** — **2001-2002**

Developed distributed virtual environment for simulation and modelling including sensor fusion; networked/distributed controls for underwater robots for intervention missions.

EDUCATION

PhD Physics — **Arizona State University** — **2008**

Modelling and Analysing the Motion of Biomolecules
High-performance / distributed / massively-parallel algorithms

MS Microbiology — **University of Hawai`i at Mānoa** — **2002**

Emphasis: virology/immunology
Applying non-linear optimisation techniques to cell culture

BS Microbiology/ BA Mathematics — **Arizona State University** — **1998**